

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,103,793 B2  
APPLICATION NO. : 10/685940  
DATED : September 5, 2006  
INVENTOR(S) : Eric M. Rentschler et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

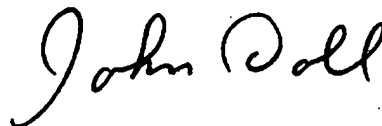
In column 5, line 61, insert -- DDR -- before "SDRAMs".

In column 22, line 16, delete "/1;2" and insert -- 1/2 --, therefor.

In column 22, line 33, delete "DQS18 RCV\_ON" and insert -- DQS18\_RCV\_ON --, therefor.

Signed and Sealed this

Third Day of February, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*